PELLISSIPPI STATE TECHNICAL COMMUNITY COLLEGE
MASTER SYLLABUS

ACTIVE DEVICES I W/ LAB
EET 1210

Class Hours: 3.0  Credit Hours: 4.0
Laboratory Hours: 3.0  Revised: Spring 05

Catalog Course Description:

An introductory course in solid-state devices and the basic circuits in which they are used. Topics include semiconductor physics, diode circuits, bipolar transistor circuit analysis and FET circuit analysis.

Entry Level Standards:

The student must have knowledge of basic DC circuits to include series and parallel circuits and network theorems such as Thevenin's and Norton's theorems. A complete understanding of Kirchhoff's Laws and power is also required.

Prerequisites:

EET 1012

Textbook(s) and Other Course Materials:

Parts Kit: Approximate Cost $15.00.

I. Week/Unit/Topic Basis:

The following is intended as a guide to the instructor. The material covered in the course may be changed by the instructor depending upon the progress, etc., of the class.

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<th>Topic</th>
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II. Course Objectives*:

A. Understand the operation and applications of the PN junction diode. A, B, D

B. Understand the operation of and how to properly bias/connect transistors and operational amplifiers. A, B, D

C. Calculate the DC and AC voltages present in transistor circuits and op amp circuits. A, B

D. Perform mid-band analysis of single and multistage small-signal bipolar and field effect transistor amplifiers and op amp circuits. A, B, D

E. Connect any of the circuits studied and make basic AC and DC measurements to verify circuit performance and reinforce the theory. A, B, D

F. Understand and perform frequency response of amplifiers. A, B, D

G. Understand the operation of thyristors and other special devices studied in the course. A, B, D

H. Demonstrate, as an individual and as a team member, library/information skills, time management skills, problem-solving skills, material management skills, and communication skills. D, F, G, H, I, J, K

*Letters after course objectives reference EET Program Outcomes (as required by ABET).

III. Instructional Processes*:

Students will:

1. Participate in classroom discussions which challenge their abilities to think creatively and visualize complex spatial and mathematical relationships to solve problems. *Mathematics Outcome*

2. Work in teams to conduct laboratory experiments and also to solve special problem assignments. These activities are designed to foster interpersonal skills in teamwork and develop and enhance leadership skills, students' abilities to express ideas, and students' abilities to reach consensus solutions for the team through negotiation. *Communication Outcome; Mathematics Outcome; Active Learning Strategy*

3. Use electronic test equipment to test electrical circuits constructed from schematics in the laboratory and acquire data. Use computers with applications software to simulate, analyze, and predict the behavior of electrical circuits. Compare expected responses to experimental
responses of electrical circuits. Use the Internet for special assignments such as locating data sheets on electronic components. Use computers with word processing software to prepare reports. Communication Outcome; Mathematics Outcome; Technological Literacy Outcome

4. Prepare reports on laboratory experiments which include methodology, mathematical analyses of electrical circuit models, a comprehensive comparison of calculated results with experimental results, and conclusions. Communication Outcome; Mathematics Outcome; Technological Literacy Outcome

5. Discuss the importance of personal qualities such as personal responsibility, time management principles, self-esteem, sociability, self-management, integrity and honesty in school and in the workplace, and dynamics of change in the workplace. Social and Behavioral Science Outcome, Transition Strategy

*Strategies and outcomes listed after instructional processes reference TBR’s goals for strengthening general education knowledge and skills, connecting coursework to experiences beyond the classroom, and encouraging students to take active and responsible roles in the educational process.

IV. Expectations for Student Performance*:

Upon successful completion of this course, the student should be able to:

1. Identify the anode and cathode of a diode. A

2. Obtain the diode characteristics from a curve tracer. A, E

3. Determine the forward and reverse breakdown voltage for any diode. A, E


5. Identify a voltage multiplier. A

6. Identify clamper circuit. A

7. Construct a zener regulator circuit. A, E

8. Explain the characteristics of a LED. G

9. Explain the operation of a photo diode. G

10. Explain the operation of an opto coupler. G

11. Properly take measurements using the Oscilloscope. E

12. Obtain transistor characteristics from a curve tracer. B, E

13. Explain the operation of the transistor as a switch. B

14. Connect circuits which will properly bias the transistor. B, E

15. Calculate and measure the DC and AC voltages present in bipolar and field-effect transistor circuits. B, C, E, H
16. Perform midband analysis of single and multistage bipolar and field-effect transistor small-signal amplifiers. D, H


18. Explain Miller’s Theorem and the use of decibels. F

19. Calculate the low-frequency response of an amplifier. F, H

20. Calculate the high-frequency response of an amplifier. F, H

21. Combine calculated low-, mid-band, and high-frequency responses to produce a complete frequency response of an amplifier. D, F, H

22. Perform frequency response measurement techniques in the laboratory on amplifiers. E, F

23. Acquire technical information from various media in the Educational Resource Center or elsewhere. H

24. Function as an effective team member in the lab or in classroom team assignments. H

25. Prepare a technical report. H

*Letters after performance expectations reference the course objectives listed above.

V. Evaluation:

A. Testing Procedures: 80% of grade

The evaluation in the classroom grade will be determined by a combination of tests, homework, and a final exam. Pop quizzes may also be used at the discretion of the instructor. The percentage that each of these factors count and the frequency of quizzes, tests and homework is left to the discretion of the instructor, but the following is offered as a guide:

Tests: At least 4 tests 75%
Homework: Approximately once per week 5%
Final Exam: 20%

B. Laboratory Expectations: 20% of grade

The laboratory grade will be determined by a combination of performance (including teamwork) within the lab and the degree of comprehension demonstrated in the lab report. There will be at least twelve labs during the semester to go along with the classroom material. The following is offered as a guide for the instructor:

Performance in labs (subjective) 20%
Lab Reports (neatness and content) 60%
Computer Program (or application) 10%
Laboratory Test 10%

Laboratory topics may vary at the discretion of the instructor, but will be related in a timely manner to the course work. The following list of topics is suggested:
A. Diode characteristics, rectifier circuits, capacitive filter
B. Diode limiter, clamper, voltage multiplication circuits, zener diode and voltage regulation
C. Bipolar transistor biasing
D. The common-emitter amplifier
E. The common-collector (emitter-follower) amplifier
F. The combination common-emitter and common-collector amplifier
G. The common base amplifier
H. FET biasing
I. The common source amplifier
J. Amplifier frequency response

C. Field Work:

N/A

D. Other Evaluation Methods:

N/A

E. Grading Scale:

93 - 100  A
88 - 92   B+
83 - 87   B
78 - 82   C+
70 - 77   C
60 - 69   D
Below 60   F

VI. Policies:

A. Attendance Policy:

Pellissippi State Technical Community College expects students to attend all scheduled instructional activities. As a minimum, students in all courses must be present for at least 75 percent of their scheduled class and laboratory meetings in order to receive credit for the course. Individual departments/programs/disciplines, with the approval of the vice president of Academic and Student Affairs, may have requirements that are more stringent. Unexcused absences and class or lab work not made up in a timely manner may very well result in a reduced grade for the course or in failure of the course. It is the student's responsibility to be present when the instructor informs the class of attendance and work requirements, or otherwise the student must contact the instructor for this information. Attendance is required to all lab sessions unless excused by the instructor. Students missing more than four unexcused sessions will receive an "F" and no credit will be received. Students tardy past half an hour will be considered absent.

B. Academic and Classroom Misconduct:

Academic misconduct committed either directly or indirectly by an individual or group is subject to disciplinary action. Prohibited activities include but are not limited to the following practices: Cheating, including but not limited to unauthorized assistance from material, people, or devices when taking a test, quiz, or examination; writing papers or reports; solving problems; or completing academic assignments. In addition to other possible disciplinary sanctions that may be imposed as a result of academic misconduct, the instructor has the authority to assign either (1) an F or zero for the assignment or (2) an F for the course.
C. Accommodations for disabilities:

If you need accommodations because of a disability, if you have emergency medical information to share, or if you need special arrangements in case the building must be evacuated, please inform the instructor immediately. Please see the instructor privately after class or in his/her office. Students must present a current accommodation plan from a staff member in Services for Students with Disabilities (SSWD) in order to receive accommodations in this course. Services for Students with Disabilities may be contacted by going to Goins 127 or 131 or by phone: 694-6751(Voice/TTY) or 539-7153.