PELLISSIPPI STATE COMMUNITY COLLEGE
MASTER SYLLABUS

CIRCUITS I
ECE 2010

Class Hours: 3        Credit Hours: 3
Laboratory Hours: 0    Date Revised: Spring 2010

Catalog Course Description:
Fundamental laws of circuit analysis: Ohm's Law, Kirchhoff's voltage and current laws, and the law of conservation of energy; circuits containing independent and dependent voltage and current sources, resistance, conductance, capacitance and inductance are analyzed using mesh and nodal analysis, superposition and source transformations, and Norton's and Thevenin's Theorems; steady state analysis of DC and AC circuits; complete solution for transient analysis for circuits with one and two storage elements.

Entry Level Standards:
Students entering this course must have college-level math skills.

Prerequisites:
CHEM 1110 and CSIT 1050

Corequisites:
PHYS 2110

Textbook(s) and Other Course Materials:

I. Week/Unit/Topic Basis:

<table>
<thead>
<tr>
<th>Week</th>
<th>Topic</th>
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<tbody>
<tr>
<td>1</td>
<td>Definitions and Units, Experimental Laws and Simple Circuits</td>
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<tr>
<td>2</td>
<td>Circuit Analysis Techniques</td>
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<tr>
<td>3</td>
<td>Inductance and Capacitance</td>
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<tr>
<td>4</td>
<td>Source Free RL and RC Circuits</td>
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<tr>
<td>5</td>
<td>Driven RL and RC Circuits</td>
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<tr>
<td>6</td>
<td>The Unit Step Forcing Functions</td>
</tr>
<tr>
<td>7</td>
<td>Source Free RLC Circuits</td>
</tr>
<tr>
<td>8</td>
<td>Driven RLC Circuits</td>
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</table>
II. Course Goals*

The course will:

A. Enhance student ability to effectively communicate with technical and scientific community in the "common language" of electrical definitions, units, and relationships. (I)

B. Expand student understanding of the analysis of circuits containing independent and dependent voltage and current sources, resistance, conductance, capacitance and inductance using basic analytical techniques developed from fundamental laws, theorems, and elementary network topology. (V, VI, VII)

C. Enhance student ability to perform steady-state analysis of DC and AC circuits. (V, VI, VII)

D. Enhance student ability to comprehend the complete solution for transient analysis for circuits with one and two storage elements. (V, VI, VII)

*Roman numerals after course objectives reference TBR’s general education goals.

III. Expected Student Learning Outcomes*

Students will be able to:

1. Itemize the basic units and derived units of the International System of Units which are of interest to circuit analysts. A

2. Define the unit of charge and understand the basic concepts of voltage, current, and power. A

3. Contrast the types of circuits and circuit elements such as resistors, capacitors, and inductors. A

4. Apply Ohm's law and use it in circuit analysis. B,C,D

5. Apply Kirchhoff's voltage and current laws in circuit analysis. B,C,D

6. Demonstrate the technique of analyzing a single-loop circuit and the single-node circuit. B

7. Calculate equivalent sources and resistances using the technique of resistance and source combination. B,C,D

8. Demonstrate voltage and current division. B,C,D
9. Identify symbols for circuit elements and write equations which describe circuit behavior. A,B,C,D

10. Understand and apply nodal analysis and mesh analysis to analyze circuits. B,C,D

11. Demonstrate source transformations in circuit analysis. B,C,D

12. Comprehend and use the concepts of linearity and superposition in circuit analysis. B,C,D

13. Understand and apply Thevenin's and Norton's theorems in circuit analysis. B,C,D

14. Describe trees and general nodal analysis, and links and loop analysis, and be able to use these in circuit analysis. B,C,D

15. Define the characteristics of the inductor and capacitor and the relationships of inductor and capacitor combinations. A,B

16. Describe the concepts of duality and linearity, and the consequences of linearity, and apply these concepts in problem solving. B,C,D

17. Recognize simple and general source-free RL and RC circuits and be able to apply analytical techniques to describe their behavior. B

18. Indicate the properties of the exponential response in RL and RC circuits. B,D

19. Describe the natural and forced responses and be able to quantify the behavior of RL and RC circuits driven by the unit-step forcing function. B,D

20. Apply analytical techniques to quantifying the behavior of source-free series and parallel RLC circuits. B,D

21. Distinguish between the concepts of over damped, critically damped, and under damped RLC circuits, and be able to analyze these circuits. B,C,D

22. Find the complete response of an RLC circuit. B,C,D

23. List the characteristics of sinusoids, and be able to calculate the forced response of circuits to sinusoidal forcing functions. B,C,D

24. Describe and apply the concepts of the complex forcing function, the phasor, and phasor relationships for R, L, and C. B,C,D

25. Define impedance and admittance and their role in circuit analysis. A,B,C,D

26. Describe and use the following in the determination of the sinusoidal steady-state response of circuits: nodal, mesh, and loop analysis; superposition, source transformations, and Thevenin's and Norton's theorems; and phasor diagrams. C

27. Describe and calculate the sinusoidal steady-state response of circuits as a function of radian frequency. C

* Capital letters after Expected Student Learning Outcomes reference the course goals listed above.

IV. Evaluation:

A. Testing Procedures:
Exams 60%
Homework and Quizzes 20%
Final Exam 20%

B. Laboratory Expectations:
None

C. Field Work:
None

D. Other Evaluation Methods:
None

E. Grading Scale:

<table>
<thead>
<tr>
<th>Score Range</th>
<th>Grade</th>
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<tbody>
<tr>
<td>93 - 100</td>
<td>A</td>
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<tr>
<td>88 - 92</td>
<td>B+</td>
</tr>
<tr>
<td>83 - 87</td>
<td>B</td>
</tr>
<tr>
<td>78 - 82</td>
<td>C+</td>
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<tr>
<td>70 - 77</td>
<td>C</td>
</tr>
<tr>
<td>60 - 69</td>
<td>D</td>
</tr>
<tr>
<td>Below 60</td>
<td>F</td>
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V. Policies:

A. Attendance Policy:

Pellissippi State expects students to attend all scheduled instructional activities. As a minimum, students in all courses (excluding distance learning courses) must be present for at least 75 percent of their scheduled class and laboratory meetings in order to receive credit for the course. Individual departments/programs/disciplines, with the approval of the vice president of the Learning Division, may have requirements that are more stringent. In very specific circumstances, an appeal of the policy may be addressed to the head of the department in which the course was taken. If further action is warranted, the appeal may be addressed to the vice president of the Learning Division.

B. Academic Dishonesty:

Academic misconduct committed either directly or indirectly by an individual or group is subject to disciplinary action. Prohibited activities include but are not limited to the following practices:
• Cheating, including but not limited to unauthorized assistance from material, people, or devices when taking a test, quiz, or examination; writing papers or reports; solving problems; or completing academic assignments.
• Plagiarism, including but not limited to paraphrasing, summarizing, or directly quoting published or unpublished work of another person, including online or computerized services, without proper documentation of the original source.
• Purchasing or otherwise obtaining prewritten essays, research papers, or materials prepared by another person or agency that sells term papers or other academic materials to be presented as one’s own work.
• Taking an exam for another student.
• Providing others with information and/or answers regarding exams, quizzes, homework or other classroom assignments unless explicitly authorized by the instructor.
• Any of the above occurring within the Web or distance learning environment.

C. Accommodations for disabilities:

Students who need accommodations because of a disability, have emergency medical information to share, or need special arrangements in case the building must be evacuated should inform the instructor immediately, privately after class or in her or his office. Students must present a current accommodation plan from a staff member in Services for Students with Disabilities (SSWD) in order to receive accommodations in this course. Services for Students with Disabilities may be contacted by going to Goins 127, 132, 134, 135, 131 or by phone: 539-7153 or TTY 694-6429. More information is available at www.pstcc.edu/departments/swd/.

D. Other Policies: